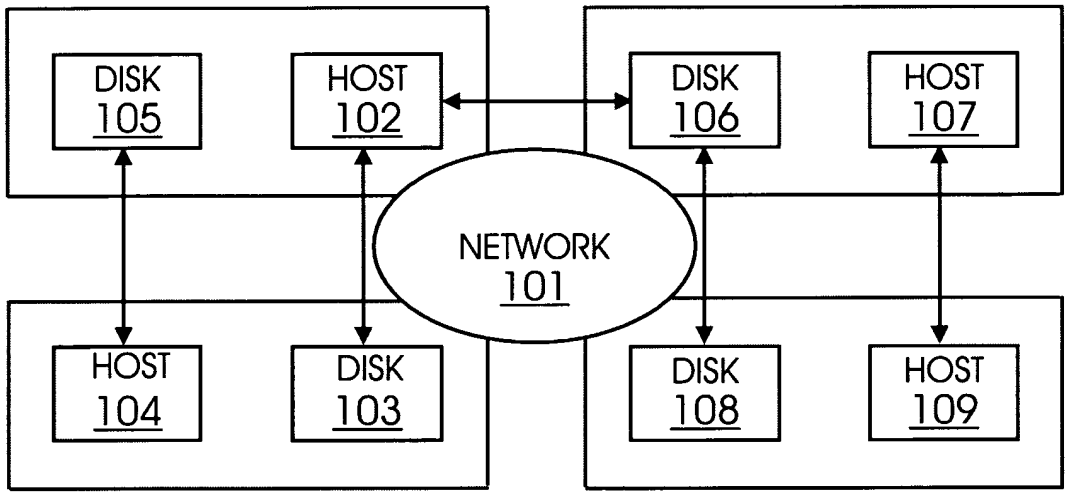
DISK
105
HOST
102
DISK
106
HOST
107
NETWORK
101
HOST
104
DISK
103
DISK
108
HOST
109
100

FIGURE 1

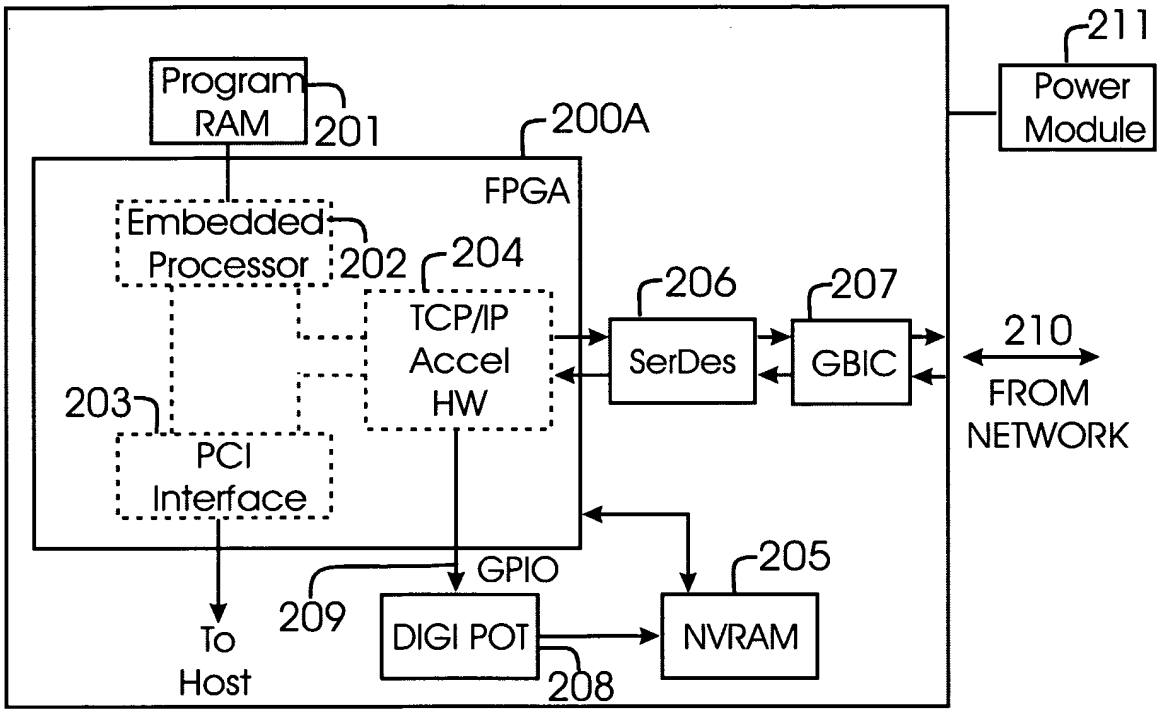

FIGURE 2A

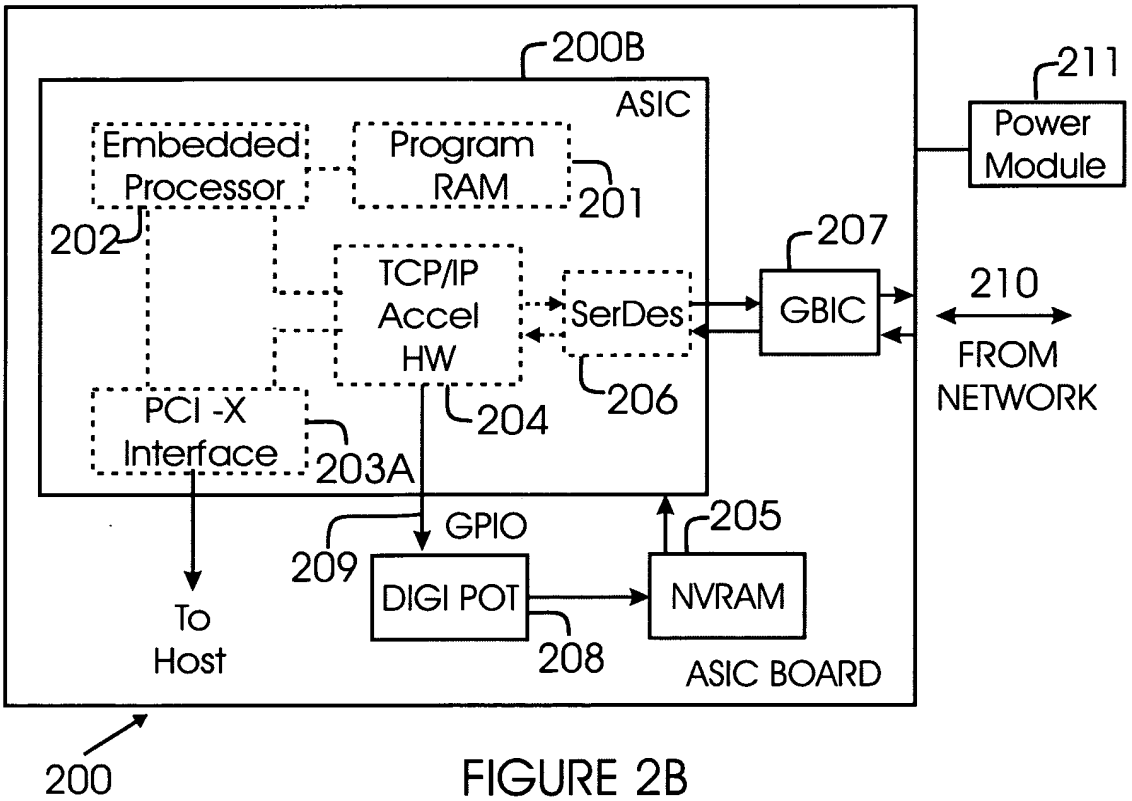

FIGURE 2B

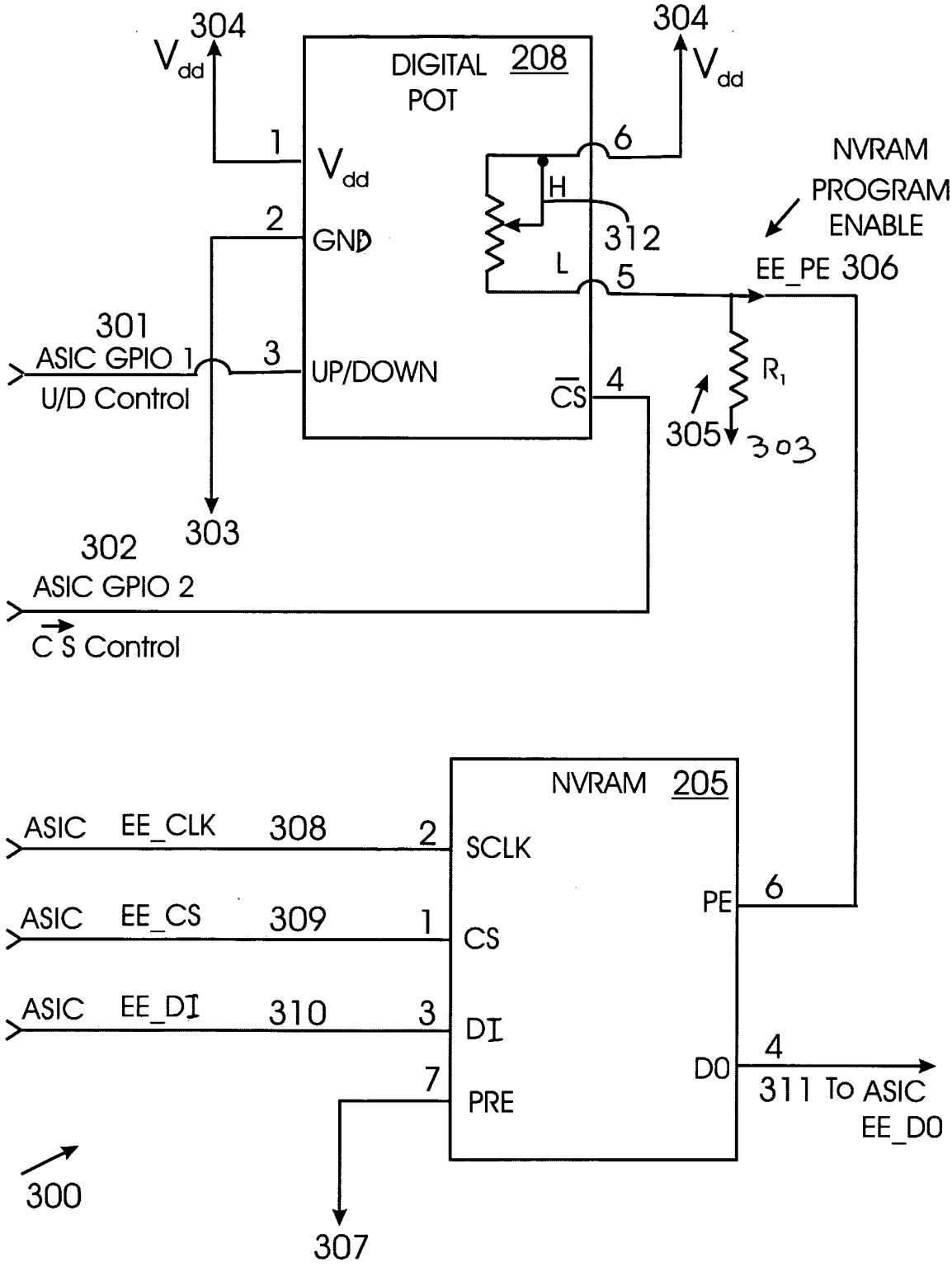
304
V_{dd}
DIGITAL 208
POT
304
V_{dd}
1
V_{dd}
6
H
NVRAM
PROGRAM
ENABLE
2
GND
312
L
5
EE_PE 306
301
ASIC GPIO 1
U/D Control
3
UP/DOWN
$\overline{CS}$
4
R_1
305
303
303
302
ASIC GPIO 2
$\overline{CS}$ Control
NVRAM 205
ASIC EE_CLK 308
2
SCLK
PE
6
ASIC EE_CS 309
1
CS
ASIC EE_DI 310
3
DI
7
PRE
DO
4
311 To ASIC
EE_DO
300
307

FIGURE 3

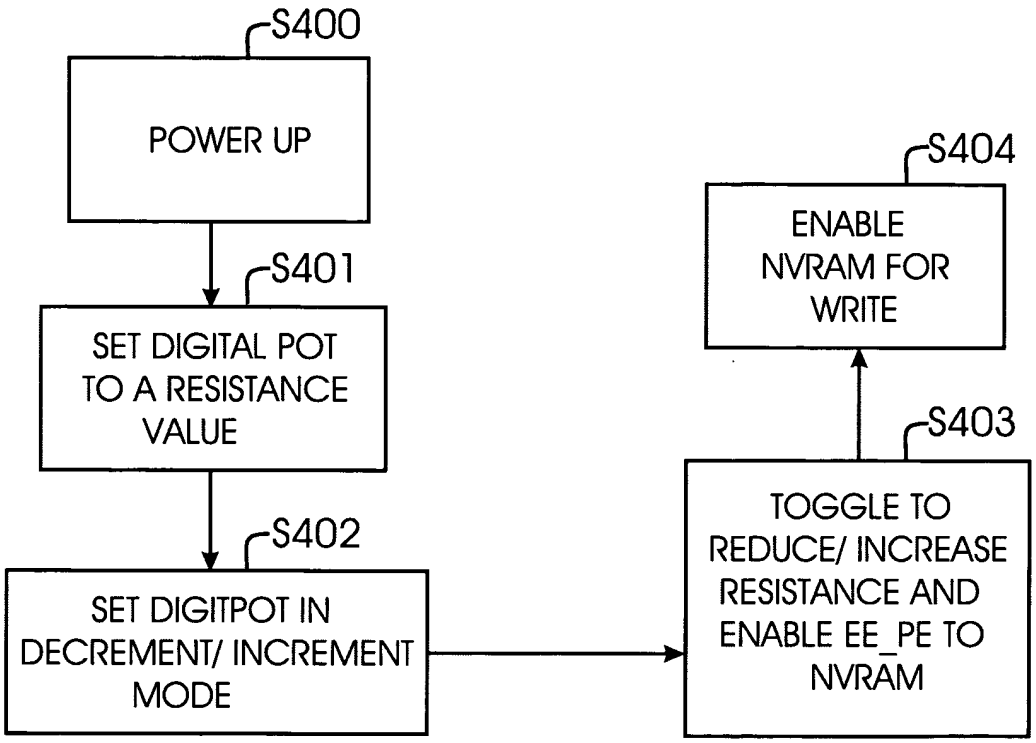
S400
POWER UP
S401
SET DIGITAL POT TO A RESISTANCE VALUE
S402
SET DIGITPOT IN DECREMENT/ INCREMENT MODE
S403
TOGGLE TO REDUCE/ INCREASE RESISTANCE AND ENABLE EE_PE TO NVRAM
S404
ENABLE NVRAM FOR WRITE

FIGURE 4